

PVSIM⁶: A Simulation Program for Photovoltaic Cells, Modules, and Arrays

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ABSTRACT

An electrical simulation model for photovoltaic cells, modules, and arrays has been developed that will be useful to a wide range of analysts in the photovoltaic industry. The Microsoft® Windows™ based program can be used to analyze individual cells, to analyze the effects of cell mismatch or reverse bias ("hot spot") heating in modules, and to analyze the performance of large arrays of modules including bypass and blocking diodes. User-defined statistical variance can be applied to the fundamental parameters used to simulate the cells and diodes. The model is most appropriate for cells that can be accurately modeled using a two-diode equivalent circuit. This paper describes the simulation program and illustrates its versatility with examples.

INTRODUCTION

A variety of numerical models are currently in use by scientists investigating different aspects of photovoltaic technologies. The fundamental physics associated with solar cells are often studied using programs that model electron/hole transport and carrier recombination mechanisms [1, 2]. Modeling of individual cells has recently been done using a highly distributed SPICE model [3]. Photovoltaic systems are often designed using a relatively simple module model coupled with site-dependent solar resources data, using PVSIM [4] or PVFORM [5]. More elaborate hybrid photovoltaic systems are designed and optimized using HYSIM [6], again with a relatively simple model for individual module performance. Our simulation program (PVSIM) was designed to more rigorously address the interactive behavior of modules in arrays by accurately simulating the electrical characteristics of individual cells in the modules. The PVSIM model was motivated by an earlier work to develop a similar computer simulation [7].

PROGRAM DESCRIPTION

In PVSIM, cell current-voltage (I-V) curves are modeled using a lumped parameter, two-diode equivalent circuit that includes a model for reverse voltage breakdown. Parameters used to define cell I-V curves include short-circuit current, series resistance, shunt resistance, ideal diode ($n=1$) saturation current, non-ideal diode saturation

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current, diode quality factor for the non-ideal diode, and cell temperature. These parameters are specified at two different cell temperatures in PVSIM enabling interpolation for user-specified cell temperatures. The simulation also includes the effects of blocking and bypass diodes in array circuits, and statistical variance in the parameters that define cell and diode characteristics.

Input to the program consists of cell and diode parameters, array configuration, and a selection of the desired output. The program produces a report consisting of I-V curve charts with operating and maximum power points indicated, array configuration information, and a performance sheet for all elements of the array selected for analysis. "Dark" I-V curves can also be simulated for cells and modules, helping analyze measurements made in the laboratory without illumination on the cells. This simulation can be used to analyze or troubleshoot existing systems, help design new systems, or as a teaching tool.

In order to appeal to the largest number of users, the simulation was developed as a Microsoft® Windows™ based program for IBM®-compatible computers. The user interface has been designed to be easy to use and intuitive. Information is entered on a series of forms. The user moves from one form to the next by clicking a mouse on the buttons shown on the form. All of the information entered by the user is saved in a resident database. This allows the user to re-use the same simulation parameters or array configuration repeatedly. Pentium™ speed processors are desirable for running large array simulations with thousands of cells.

ARRAY TERMINOLOGY

The usual way to specify the arrangement of cells and modules in an array is to use hierarchical terminology such as cell, cell-string, module, module-string, etc. Eight hierarchical levels were selected for use in PVSIM; they are believed to be in somewhat common usage within the photovoltaic industry:

1. Array
2. Sub-array (source circuit)
3. Parallel module-string

4. Series module-string
5. Module
6. Parallel cell-string
7. Series cell-string
8. Cell

CELL SIMULATION MODEL

The PVSIM model starts with fundamental cell characteristics for each cell in an array. The lumped-parameter, two-diode equivalent circuit used for each cell is defined by Equations 1-5. This two-diode circuit works well for a wide variety of solar cell technologies and temperatures. The model includes parameters for an ideal diode ($n=1$) and a non-ideal ($n \neq 1$) diode, shunt and series resistance, short-circuit current, reverse breakdown parameters, as well as cell temperature. Photon generated current is calculated using Eq. 1 given the cell parameters and the short-circuit current (I_{sc}).

$$I = I_p - I_{d1} - I_{d2} - I_{sh} \quad (1)$$

where:

- I = Cell current (A)
- I_p = Photon generated current (A)
- I_{d1} = Ideal diode current (A)
- I_{d2} = Non-ideal diode current (A)
- I_{sh} = Shunt current (A)

The currents are calculated from the following equations:

$$I_{d1} = I_{o1} \left\{ e^{\frac{q}{kT}(V_j)} - 1 \right\} \quad (2)$$

$$I_{d2} = I_{o2} \left\{ e^{\frac{q}{n_2 kT}(V_j)} - 1 \right\} \quad (3)$$

$$I_{sh} = \frac{V_j}{R_{sh}} \left\{ 1 + a \left(1 - \frac{V_j}{V_{br}} \right)^{-m} \right\} \quad (4)$$

$$V_j = V + I R_s \quad (5)$$

where:

- I = Cell current (A)
- V = Cell voltage (V)
- R_s = Series resistance (Ω)
- R_{sh} = Shunt resistance (Ω)
- q = Elementary charge = 1.602177×10^{-19} (C)
- k = Boltzmann's constant = 1.380662×10^{-23} (J/K)
- T = Junction temperature (K)
- V_j = Calculated junction voltage (V)
- I_{o1} = Ideal diode recombination current (A)
- I_{o2} = Non-ideal diode recombination current (A)
- V_{br} = Junction breakdown voltage (V)
- n_2 = Non-ideal diode quality factor
- a = Fraction of shunt current involved in breakdown
- m = Avalanche breakdown exponent

Parameters for each cell in an array are generated statistically so that cell-to-cell variance is included; the user specifies the standard deviation for each parameter. As an option, the user may specify cell parameters exactly. Individual cells may also be open-circuited or short-circuited, or have their parameters altered independent of other cells as desired. These provisions make it possible to analyze the effects of cell or module mismatch on array performance, and to analyze the influence of component failures. Another advantage of the simulation is that temperature coefficients for current and voltage are implicitly contained in the fundamental cell parameters. Translating from one irradiance and temperature condition to another does not require the coefficients used in standard ASTM methods [8].

Bypass and blocking diodes can both be included in the simulation of an array. A lumped-parameter single-diode equivalent circuit with shunt and series resistance is used to simulate the diodes. Parameters are also included to simulate diode reverse breakdown and temperature effects. The diode model is similar to the cell model except that there is no photon generated current and only one diode current. Parameters for each diode in the array can be generated statistically or specified exactly. Diodes may also be open-circuited or short-circuited.

PARAMETER DETERMINATION

The photovoltaic literature has contained papers describing techniques for extracting one or more of the parameters used in the solar cell equivalent circuit previously discussed for over thirty years [9, 10]. The use of the two-diode equivalent circuit provides a better match to solar cell performance over a wider range of operating conditions than a single-diode circuit. However, the process for determining "physically meaningful" cell parameters that fit measured I-V data over a wide range of operating conditions becomes more problematic. The method preferred by the authors uses nonlinear regression analysis of dark I-V measurements at cell temperatures of 25 °C and 75 °C to obtain two sets of values for I_{o1} , I_{o2} , n_2 , R_s , and R_{sh} [11]. The values for I_{sc} are determined using conventional illuminated I-V measurements at the same temperatures. Interpolation is then used in PVSIM to find appropriate values for the parameters at temperatures between these two extremes. By determining parameters for a production sample of cells, parameters as well as an estimate of their production variance can be determined.

EXAMPLES

PVSIM provides a variety of analytical results from the simulation. The first is a chart of I-V curves as pictured in Fig. 1. The chart shows the user selected I-V curves with each one drawn in a different color and individually labeled. Maximum power points are shown as small diamonds and operating points are shown as small circles. The case illustrated is a 36-cell crystalline silicon module with bypass diodes around every twelve cells,

and with one substandard cell producing about 10 percent less current than the others.

The second example, Fig. 2, illustrates the diagnostic capability of PVSIM for an old 2-kW array (1920 cells) with wiring problems. The array is composed of four large modules (panels) connected in series. Each module is composed of 30 parallel cell-strings, each cell-string with 12 cells in series. For the simulation, three cell-strings were open circuited in one module, six cell-strings in a second module, and nine in the third module.

In addition to I-V curve charts, the user may print a report which includes array performance parameters and setup information. Performance information includes the maximum power point, operating point, short circuit current, open circuit voltage, fill factor, and operating state for each I-V curve selected in the initial setup. Bypass and blocking diode operating conditions are also given indicating whether they are conducting normally or dissipating energy.

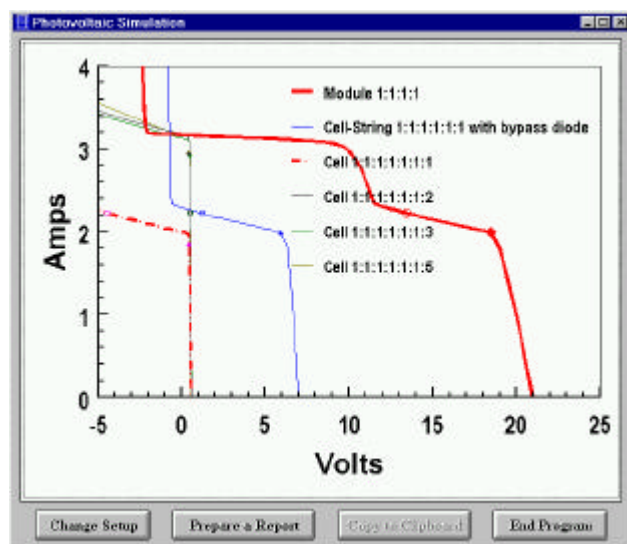


Fig. 1. 36-cell c-Si module with three bypass diodes and one weak cell.

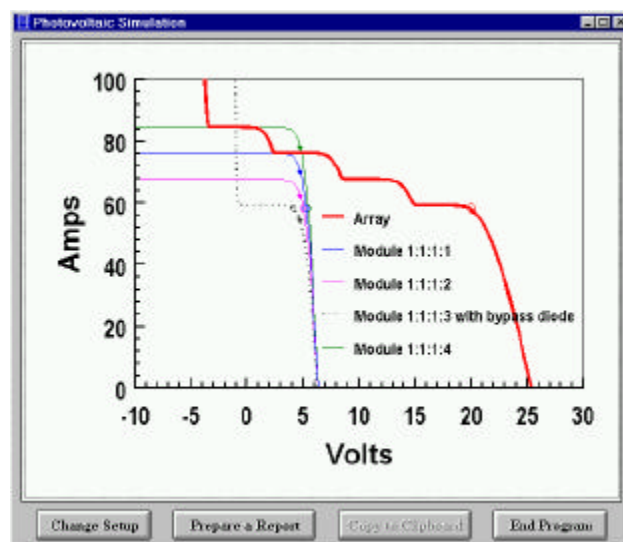


Fig. 2. Old 2-kW c-Si array with bypass diodes and several open-circuited cell-strings.

CONCLUSIONS

A Microsoft® Windows™ program has been developed for simulating the electrical performance of cells, modules, and arrays. A beta-version of the software has been distributed to about twenty potential users. Continued development of the program with the intent of more general distribution will depend on the level of interest shown by the photovoltaic community. Please forward inquiries regarding software availability to the following address.

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